

Sheet 1 of 1

## INFORMATION DISCLOSURE STATEMENT

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U. S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEAPPLICANT  
Eiji UEDALIST OF REFERENCES CITED BY APPLICANT(S)  
(Use several sheets if necessary)FILING DATE  
July 31, 2006

GROUP

Date Submitted to PTO: July 31, 2006

## U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/KE/	AA	2002/0071303	6/2002	Kund et al.			
/KE/	AB	2003-0212871	11/2003	Suzuki et al.			
/KE/	AC	2001-0033012	10/2001	Koemmerling et al.			
/KE/	AD	2004/0062115	4/2004	Takeuchi et al.			
	AE						
	AF						
	AG						
	AH						
	AI						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
/KE/	AJ	2003-91988	3/2003	JP			Abstract
	AK						
	AL						
	AM						
	AN						

## OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

/KE/	AO	K. Asari et al., "Ferroelectric Memory Circuit Technology and the Application to Contactless IC Card," IEICE TRANSACTIONS ON ELECTRONICS, Institute of Electronics Information and Comm. Eng., Tokyo, Japan, vol. E81-C, no. 4, April 1998 (4/1998), pp. 488-495, XP000833773
	AP	
	AQ	

EXAMINER /Kevin Ellis/

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